

| | Issue Date | Pages | Title | Document ID | Current OR | Current XRef |
|---|------------|-------|--|-------------------|------------|-------------------------------|
| 1 | 20020221 | 43 | Liquid crystal display device and method of driving the same | US 20020021295 A1 | 345/205 | 345/87 |
| 2 | 19940920 | 21 | Electro-optical device and process for fabricating the same and method of driving the same | US 5349366 A | 345/92 | 345/206; 349/37; 349/48 |

| | Type | L # | Hits | Search Text | DBs |
|----|------|-----|--------|-----------------------------------|---|
| 1 | BRS | L1 | 190904 | pixel or (display adj element) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 2 | BRS | L2 | 119812 | nonvolatile or non-volatile | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 3 | BRS | L3 | 10178 | memory adj circuits | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 4 | BRS | L4 | 103 | 1 and 2 and 3 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 5 | BRS | L5 | 50510 | 345/\$.cc1s. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 6 | BRS | L6 | 21 | 4 and 5 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 7 | BRS | L8 | 736 | 345/89.cc1s. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 8 | BRS | L9 | 14 | 1 and 2 and 8 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 9 | BRS | L10 | 677 | SRAM and 1 and 5 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 10 | BRS | L11 | 14 | 8 and 10 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 11 | BRS | L12 | 363 | (345/205-206) .cc1s. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| 12 | BRS | L13 | 30181 | FeRAM or SRAM | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |

| | Type | L # | Hits | Search Text | DBs |
|----|------|-----|------|--|---|
| 13 | BRS | L14 | 2437 | 1 and 13 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |
| 14 | BRS | L15 | 677 | 5 and 14 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |
| 15 | BRS | L16 | 9 | 3 and 12 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |
| 16 | BRS | L17 | 108 | francis near nguyen | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |
| 17 | BRS | L18 | 2745 | volatile and (memory adj circuit) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |
| 18 | BRS | L19 | 1456 | nonvolatile and (memory adj circuit) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |
| 19 | BRS | L20 | 699 | 18 and 19 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |
| 20 | BRS | L21 | 33 | 1 and 20 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |
| 21 | BRS | L22 | 2 | 5793344.pn. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |
| 22 | BRS | L23 | 2 | 12 and 20 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB |